

Jose L. Agraz, PhD. Biography

Jose L. Agraz, PhD is a product design/validation engineer at Intel with the MVE group. Dr Agraz became fascinated by engineering research in his early teens. At the time, he put together an experiment in magnetics using junk found on the beach and scrapped wire. As he plugged in the device into the wall socket, his engineering career began with a spark!!

Dr Agraz earned his PhD from the University of California, Los Angeles (UCLA) and Northwestern University in Chicago, and completed his graduate research at Cedars-Sinai Medical Center in Hollywood, CA. While at Cedars-Sinai, his graduate research focused in Spin-Transfer through Hyperpolarization Instrumentation for Cancer Imaging at the Molecular level. His research in spin transfer imaging was showcased at the San Diego Museum of Science Teachers Workshop in 2015. He completed his Postdoc at the Pozos laboratory in San Diego State University (SDSU) with a focus in hardware and software development for big data research. He received his master's in Electrical Engineering at Pennsylvania State University, with focus in mixed signal circuits and miniature piezoelectric linear motors. He also received a Code Division Multiple Access (CDMA) Engineering Certificate from the University of California, San Diego (UCSD), and his bachelor's in Electrical Engineering at SDSU with a focus in mixed signal circuits applications in electrophysiology, more specifically, finger force measurements for the quantification of carpal tunnel syndrome injuries, earning the SDSU Innovation technology Award and first place with the IEEE SDSU student chapter. He holds five patents and authored 13 research papers, seven of these as first author. He has over 20 years of experience in the high-tech industry, making him an expert in bringing theory to a working prototype. He has held a myriad of positions ranging from electronic technician to small business CEO, beginning with Helle Engineering, Ectron Corp, Air Force Space Command, Cadence Systems Design, Baja Education, and co-founder of Ergostress Corp and Agraz Research LLC. He was a finalist at Intel Venture 2017 with a wearable device for the Precognition of Falls in the Elderly, opening the \$7T elderly care market to Intel and lead the microphone echolocation project for the Intel Bot Solutions Venture. Currently, he leads Intel collaborations with Pennsylvania State University and UCLA in the precognition of falls in elderly and artificial intelligence for the enhancement of low resolution imaging research respectively. He is a member of the Intel patent reviewing committee and a board member for the Community Partners for Affordable Housing in Tigard Oregon.